

1 **WHAT IS CLAIMED IS:**

2 1. A real time data compression method for a data recorder that
3 sequentially reads a plurality of sampled data points, the method comprising the
4 steps of:

5 setting the first sampled data point as a starting point X_0 ;
6 sequentially examining the plurality of data points, wherein a present
7 data point X_i being examined is determined whether it is in a predetermined
8 tolerable error range; wherein if the present data point X_i is in the predetermined
9 tolerable error range, a previous data point X_{i-1} is ignored;

10 wherein the previous data point X_{i-1} and the total amount of data points
11 accumulated from the starting point X_0 to the previous data point X_{i-1} are
12 recorded, and the previous data point X_{i-1} is then set as a new starting point,
13 when (a) the present data point X_i exceeds the predetermined tolerable error
14 range or (b) the amount of the ignored data points reaches to a first predetermined
15 amount;

16 wherein during a period that the sampled data points are varied quickly,
17 and when the amount of the continuously examined data points is less than a
18 second predetermined amount, the total amount of the data points that exceed
19 their respective predetermined tolerable error ranges and the value of each of
20 these data point are recorded.

21 2. The method as claimed in claim 1, wherein the second predetermined
22 amount is less then the first predetermined amount.

23 3. The method as claimed in claim 1, wherein the total amount of these
24 data points that are continuously recorded when the sampled data points are

1 varied quickly is expressed by a negative number.

2 4. The method as claimed in claim 1, wherein a data format is provided to
3 store all recorded data points, the data format comprising:

4 a main region composed of multiple segments, wherein each segment
5 has a predetermined length and is provided to store the value of a presented
6 recorded data point and an amount value of the data points calculating from a
7 previous recorded data point to the present recorded data point; and

8 a secondary region for recording a start time of the examining process, a
9 value of sampling time interval and multiple blocks, wherein each block
10 corresponds to several segment in the main region and includes a time value and
11 a position value respectively representing a start time of the block and the start
12 position of the block.

13 5. The method as claimed in claim 4, wherein the time parameter of each
14 block is calculated relative to the start time of the examining process and is
15 expressed by multiples of sampling time interval.

16 6. The method as claimed in claim 4, wherein in a first segment of the
17 main region, the amount value is set to zero.

18 7. The method as claimed in claim 4, wherein data of all recorded data
19 points is firstly stored in the main region, and then in the secondary region.

20 8. The method as claimed in claim 5, wherein data of all recorded data
21 points is firstly stored in the main region, and then in the secondary region.

22 9. The method as claimed in claim 6, wherein data of all recorded data
23 points is firstly stored in the main region, and then in the secondary region.

24 10. An apparatus for real time data compression applied for a data

1 recorder, the apparatus comprising:

2 a processing unit (30) for compressing data by variable compression

3 format based on the stability of the data;

4 an interface unit (40) connected to the processing unit (30) through an

5 interface, wherein the interface unit (40) further includes a microprocessor and

6 an A/D converter (42) that converts analog measured signals to a digital form and

7 then transmits the digital signals to the microprocessor, whereafter the

8 microprocessor transmits the digital signals to the processing unit (30); and

9 a storing unit (50) connected to the processing unit (30) through

10 data/address bus for storing the compressed data;

11 wherein the interface unit (40) is adapted to connect to a measuring

12 device and receives the analog measured signals therefrom, when the analog

13 measured signals are converted into the form of digital, the digital signals are

14 compressed by the processing unit (30) and then stored in the storing unit (50).

15 11. The apparatus as claimed in claim 10, wherein the processing unit

16 (30) further connects to a real time clock (32) and a flash memory(33), where the

17 real time clock (32) regularly generates an interrupt requirement to inform the

18 processing unit (30) for data recording and compressing.

19 12. The apparatus as claimed in claim 10, wherein the interface for

20 connection between the processing unit (30) and interface unit (40) is composed

21 of two serial ports (31)(41) that are respectively provided at the processing unit

22 (30) and interface unit (40).

23 13. The apparatus as clamed in claim 10, wherein the interface unit (40)

24 connect to a measuring device and receives the analog measured signal , and the

1 processing unit (30) further connects a display unit (60).

2 14. The apparatus as claimed in claim 10, wherein the storing unit (50)
3 stores the data , the data structure comprising:

4 a main region composed of multiple segments, wherein each segment
5 has a predetermined length and is provided to store the value of a presented
6 recorded data point and an amount value of the data points calculating from a
7 previous recorded data point to the present recorded data point; and

8 a secondary region for recording a start time of the examining process, a
9 value of sampling time interval and multiple blocks, wherein each block
10 corresponds to several segment in the main region and includes a time value and
11 a position value respectively representing a start time of the block and the start
12 position of the block.

13 15. The apparatus as claimed in claim 14, wherein the time parameter of
14 each block is calculated relative to the start time of the examining process and is
15 expressed by multiples of the value of sampling time interval.

16 16. The apparatus as claimed in claim 10 further comprising a power
17 supply (70) to provide an operating voltage to the processing unit (30), the
18 interface unit (40) and the storing unit (50).